

**Listing and Amendments to the Claims**

This listing of claims will replace the claims that were filed with the international examination request of the present application:

1.(currently amended) Device for driving a plasma display panel having a plurality of cells arranged in rows and columns, the rows of cells being distributed in a plurality of block of lines (~~B1, B2~~), said device comprising row address means for selectively addressing the display cell rows within the blocks and creating, where required, in cooperation with means for selectively applying data voltages to the display columns, an electrical discharge inside the cell disposed at the intersection of the row and column selected during an address phase, and sustain means for sustaining the electrical discharges inside said cell during a sustain phase immediately following the address phase,

wherein ~~characterized in that~~ said row address means comprise row address means for addressing successively the blocks of rows by applying a first voltage (~~V<sub>bw1</sub>~~) to the cells of the selected block and a second voltage (~~V<sub>bw2</sub>~~) to the cells of the other blocks, said second voltage being greater than the first voltage (~~V<sub>bw1</sub>~~) and ~~in that~~ said row address means and/or sustain means are capable of allowing a bi-directional current to flow within the cells of the display during said address and/or sustain phases.

2.(currently amended) Device according to Claim 1, ~~characterized in that~~ wherein the row address means for each block of rows (~~B1, B2~~) comprises:

- at least one row driver circuit (~~I1~~) for each of the blocks of rows connected between the first and second connection lines (~~L1, L2; L1', L2'~~) and designed to apply, during an address phase specific to said block of rows, the potential of one of said first and second connection lines to a first electrode (~~Yas~~) of the cells of a plurality of rows of the block,

- a first switch (~~I2; I2'~~) for selectively applying an address voltage (~~V<sub>w</sub>~~) to the second connection line (~~L2; L2'~~) during the address phase,

- a first diode (~~D1; D1'~~) connected in series with a second switch (~~I1; I1'~~) for applying said first voltage (~~V<sub>bw1</sub>~~) to the first connection line (~~L1;~~

~~L1'~~ during the address phase, said diode being oriented so as to allow a current to flow in the direction of the first connection line ~~(L1; L1')~~,

- a capacitor ~~(C1; C1')~~ for connecting the cathode of the first diode ~~(D1; D1')~~ to the second connection line ~~(L2; L2')~~,

- a switching means ~~(I10; I10')~~ for isolating said first connection line ~~(L1; L1')~~ from the sustain means of said driver device during the row address phase of the relevant block, and

- a third switch (I8) for applying said second voltage (~~V<sub>bw2</sub>~~) to said first connection line ~~(L1; L1')~~ during the address phases specific to the other blocks.

3.(currently amended) Device according to Claim 2, wherein ~~characterized in that~~ said third switch (I8) is common to the address means of the blocks of rows.

4.(currently amended) Device according to Claim 3, ~~characterized in that~~ wherein the switching means is a switch ~~(I10; I10')~~ connected between the sustain means of the device and said first connection line ~~(L1; L1')~~, which switch is open during the row address phase of the relevant block.

5.(currently amended) Device according to ~~one of Claims~~ Claim 2 to 4, ~~characterized in that~~ wherein said second voltage (~~V<sub>bw2</sub>~~) is equal to said high sustain voltage (~~V<sub>s</sub>~~).

6.(currently amended) Device according to ~~any one of Claims~~ Claim 2 to 5, ~~characterized in that~~ wherein the sustain means comprise:

- third and fourth switches ~~(I3; I4)~~ for selectively applying a high sustain voltage (~~V<sub>s</sub>~~) and a low sustain voltage to said first connection line ~~(L1; L1')~~ of the blocks when the switching means of said blocks is in the on state,

- fifth and sixth switches ~~(I5; I6)~~ for selectively applying said high sustain voltage (~~V<sub>s</sub>~~) and said low sustain voltage to a second electrode (~~Y~~) of the cells of the plurality of rows selected by said row driver circuit ~~(I1)~~, said third and sixth switches ~~(I3; I6)~~ on the one hand, and said fourth and fifth transistors ~~(I4; I5)~~ on the other, being controlled in an identical manner.

7.(currently amended) Device according to Claim 6, ~~characterized in that wherein~~ the sustain means additionally comprise:

- a second diode ~~(D3)~~ connected in series with said third switch ~~(I3)~~ and oriented so as to allow a current to flow into the first connection line ~~(L1; L1')~~ of the blocks when the switching means of said blocks is in the on state, and
- third and fourth diodes ~~(D7, D8)~~ connected in parallel with the third and fourth switches ~~(I3, I4)~~, respectively, and
- fifth and sixth diodes ~~(D5, D6)~~ connected in parallel with the fifth and sixth switches ~~(I5, I6)~~, respectively.

8.(currently amended) Device according to Claim 3 ~~one of Claims 3 to 7~~, ~~characterized in that wherein~~ said third switch ~~(I8)~~ is connected in parallel with said second diode ~~(D3)~~.

9.(currently amended) Device according to Claim 6 ~~one of Claims 6 to 8~~, ~~characterized in that wherein~~ the sustain means additionally comprise a fifth switch ~~(I7; I7')~~ inserted between the first and second connection lines of each block, which switch is open during the row address phase of the relevant block and closed during the sustain phase.

10.(currently amended) Device according to Claim 9, ~~characterized in that wherein~~ the switching means for isolating the first connection line ~~(L1; L1')~~ from the sustain means of said driver device during the row address phase of the relevant block is a diode ~~(D10; D10')~~ connected between the sustain means of the device and said first connection line ~~(L1; L1')~~, which diode is oriented so as not to allow a current to flow in the direction of the first connection line ~~(L1)~~ and in that the fifth switch ~~(I7, I7')~~ is inserted between the sustain means of the device and said second connection line ~~(L2, L2')~~.

11.(currently amended) Plasma display panel ~~characterized in that wherein~~ it comprises a driver device according to ~~one of Claims 1 to 10~~ Claim 1.